

**Company:** Arteris  
**Company Description:** Arteris is a leading provider of system IP for the acceleration of system-on-chip (SoC) development across today's electronic systems. Arteris network-on-chip (NoC) interconnect IP and SoC integration automation technology enable higher product performance with lower power consumption and faster time to market, delivering better SoC economics for its customers' innovations.  
**Nomination Category:** Company / Organization Categories  
**Nomination Sub Category:** Most Innovative Tech Company of the Year - Up to 2,500 Employees  
**Nomination Title:** Arteris



1. Which will you submit for your nomination in this category, a video of up to five (5) minutes in length about the achievements of the nominated organization since 1 January 2023, OR written answers to the questions for this category? (Choose one):

Written answers to the questions

2. If you are submitting a video of up to five (5) minutes in length, provide the URL of the nominated video here, OR attach it to your entry via the "Add Attachments, Videos, or Links to This Entry" link above, through which you may also upload a copy of your video.
3. If you are providing written answers for your submission, you must provide an answer to this first question: Briefly describe the nominated organization: its history and past performance (up to 200 words):

Total 181 words used.

Arteris, founded in 2004, pioneered commercially available network-on-chip (NoC) IP technology that forms the connectivity fabric within semiconductors and accelerates their development. Its NoC interconnect IP and system-on-chip (SoC) integration software boost performance, reduce power consumption, and shorten time to market — delivering better SoC economics across automotive, AI, consumer, enterprise, and industrial markets.

With the acquisitions of Magillem and Semifore, Arteris has solidified its leadership in SoC integration and hardware/software interface (HSI) development, helping customers speed up SoC design. Its technology is embedded in over 3.75 billion SoCs used by 200+ customers including Mobileye, NXP, Samsung, Bosch, and Tenstorrent to name a few.

Arteris collaborates with key partners such as Arm, Synopsys, and Cadence, and joined the Intel Foundry Accelerator Ecosystem in 2025 to support advanced semiconductor designs. Publicly listed on Nasdaq (AIP) since 2021, Arteris continues to scale with innovation at its core.

With 250+ engineers and domain experts globally, the company expanded its footprint in 2025 by opening an engineering hub in Krakow, Poland, complementing its U.S. headquarters, R&D centers in France, and offices across Asia.

4. If you are providing written answers for your submission, you must provide an answer to this second question: Outline the organization's achievements since the beginning of 2023 that you wish to bring to the judges' attention (up to 250 words):

Total 239 words used.

Arteris continues to lead innovation in on-chip connectivity and SoC integration automation, helping semiconductor companies accelerate design and manage rising complexity. In the past year, the company has expanded its global customer and partner network while delivering key enhancements across its product portfolio.

Recent product milestones include the launch of FlexGen smart NoC IP, designed for dynamic and configurable interconnect; the next generation of Magillem Registers, which redefines hardware/software interface (HSI) development; FlexNoC 5, featuring physically aware NoC interconnect IP; new NoC tiling techniques for AI and ML-centric SoC designs; and an expanded Ncore cache coherent interconnect IP for advanced electronics. Arteris now holds 100 issued patents, with over 100 additional applications pending.

Arteris NoC IP is processor-agnostic — compatible with Arm, RISC-V, and other ISAs — and supports a wide array of industry protocols. Since early 2023, Arteris has collaborated on projects with Intel Foundry, Andes Technology, Arm, Esperanto Technologies, Fraunhofer IESE, MIPS, Semidynamics, and SiFive to deliver verified, high-performance solutions.

The company's technology is ISO 9001 certified, and its Ncore product is ISO 26262 ASIL D certified, enabling safety-critical applications in automotive and beyond.

As semiconductor designs become increasingly complex — incorporating multi-core processors, AI/ML engines, graphics, memory, safety, and security — demand for robust system IP is surging. Arteris addresses this with solutions that enable efficient data movement, optimize power and area, and accelerate time to market, making it a strategic partner for next-generation SoC innovation.

5. If you are providing written answers for your submission, you must provide an answer to this third question: Explain why the achievements you have highlighted are unique or significant. If possible compare the achievements to the performance of other players in your industry and/or to the organization's past performance (up to 250 words):

Total 234 words used.

Modern SoCs are increasingly dependent on advanced NoC technology to deliver the performance and scalability necessary to support evolving demands. NoC design is complex, laborious and error prone, particularly for SoCs targeted to run AI applications, especially AI datacenter and automotive self-driving solutions that may have hundreds or even thousands of processing elements.

SoC designers must now contend with expanding architectural complexity, accelerated time-to-market requirements, a shortage of specialized expertise, inefficient resource utilization, and fragmented toolchains. Instead of just processing data, modern electronics require SoCs that take decisions.

This is exactly where Arteris' technology comes in. The company has established itself as an essential player in the industry by addressing the growing complexity. The technology is geared toward optimizing performance, power, and efficiency — solutions that are critical for modern electronic systems.

Arteris' technological impact is largely unique because it pioneered commercial NoC IP and it has been continuously advancing the technology for the past 20 years. It offers industry-leading support and is focused on the success of its customers. Its NoC interconnect IP has been integrated into 3.75+ billion electronic devices across diverse markets.

Arteris' rapid growth, ISO certifications, and partnerships with companies like Arm and SiFive demonstrate technological foresight and help redefine industry benchmarks. Recent product expansions have driven faster and more efficient chip design cycles. Paired with an impressive patent portfolio, these advancements reinforce Arteris' role as an innovation leader.

6. You have the option to answer this final question: Reference any attachments of supporting materials throughout this nomination and how they provide evidence of the claims you have made in this nomination (up to 250 words):

Total 169 words used.

- Arteris Revolutionizes Semiconductor Design with FlexGen — Smart Network-on-Chip IP Delivering Unprecedented Productivity Improvements and Quality of Results: <https://www.arteris.com/press-releases/arteris-revolutionizes-semiconductor-design-with-flexgen/>
- Arteris Releases the Latest Generation of Magillem Registers to Automate Semiconductor Hardware/Software Integration: <https://www.arteris.com/press-releases/arteris-releases-the-latest-generation-of-magillem-registers-to-automate-semiconductor-hardware-software-integration/>
- Arteris Joins Intel Foundry Accelerator Ecosystem Alliance Program to Support Advanced Semiconductor Designs: <https://www.arteris.com/press-releases/arteris-joins-intel-foundry-accelerator-ecosystem-alliance-program-to-support-advanced-semiconductor-designs/>
- Arteris Selected by Nextchip to Accelerate Chip Designs for Automotive Vision Technology: <https://www.arteris.com/press-releases/arteris-selected-by-nextchip-to-accelerate-chip-designs-for-automotive-vision-technology/>
- Arteris case study — Intuitive: <https://www.arteris.com/resources/case-studies/intuitive/>
- Arteris case study — Dream Chip: <https://www.arteris.com/resources/case-studies/dream-chip/>

*Thought-leadership examples by the team:*

- Michal Siwinski, CMO at Arteris, for EE Times: <https://www.eetimes.com/podcasts/automating-noc-design-masters-soc-complexity/> ;
- Andy Nightingale, VP of Product Marketing at Arteris — Interview Series for Unite.ai: <https://www.unite.ai/andy-nightingale-vp-of-product-marketing-at-arteris-interview-series/>
- Arteris featured in EEJournal: Artificial Intelligence (AI) Meets Networks-on-Chip (NoCs) in More Ways Than One <https://www.eejournal.com/article/artificial-intelligence-ai-meets-networks-on-chip-nocs-in-more-ways-than-one/> ;
- Guillaume Boillet, Senior Director of Strategic Marketing at Arteris, for SemiWiki: How Arteris is Revolutionizing SoC Design with Smart NoC IP <https://semiwiki.com/events/355772-how-arteris-is-revolutionizing-soc-design-with-smart-noc-ip/> ;

Attachments/Videos/Links:

Arteris

[REDACTED FOR PUBLICATION]